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Vasanth Venkatachalam, Michael Franz

September 2005 **ACM Computing Surveys (CSUR)**, Volume 37 Issue 3

**Publisher:** ACM Press

Full text available:  pdf(602.33 KB) Additional Information: full citation, abstract, references, index terms

Power consumption is a major factor that limits the performance of computers. We survey the “state of the art” in techniques that reduce the total power consumed by a microprocessor system over time. These techniques are applied at various levels ranging from circuits to architectures, architectures to system software, and system software to applications. They also include holistic approaches that will become more important over the next decade. We conclude that power management is a ...

**Keywords:** Energy dissipation, power reduction

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# Luca Benini, Giovanni de Micheli

April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**,

Volume 5 Issue 2

**Publisher:** ACM Press

Full text available:  pdf(385.22 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survey ...

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L1	17733783	@ad<"19990414"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:10
L2	60	(Eric near2 Stubbs).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:11
L3	162	(Gordon near2 Roberts).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:11
L4	34	2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:13
L5	121535	semiconductor near2 chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:13
L6	228220	(monitor\$4 or detect\$4) near2 (presen\$4 or available)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:15
L7	61478	open adj2 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:15
L8	115768	DRAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:15
L9	8474	data near2 refresh\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:15

## EAST Search History

L10	1611	CAS near2 latency	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:16
L11	1613	5 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:16
L12	42	11 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:16
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L17	72884	"365"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:17
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L19	5	14 and 18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:18

## EAST Search History

L20	3	1 and 19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/09 08:18
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| <a href="#">#5</a> | (CAS near latency<IN>metadata)   |
| <a href="#">#6</a> | ((DRAM and (refresh rate)<IN>metadata)) <AND> ((open circuit<IN>metadata))   |
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| <a href="#">#8</a> | ((data refresh<IN>metadata)) <AND> ((CAS near latency<IN>metadata))  |
| <a href="#">#9</a> | (((((detect* or monitor*) and (memory presen*))<in>metadata)) <AND> (((DRAM and (refresh rate)<IN>metadata)) <AND> ((open circuit<IN>metadata)))) <AND> (((data refresh<IN>metadata)) <AND> ((CAS near latency<IN>metadata)))) |

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